



Docket No.: 21.1937

In re the Application of:

Hiroshi YOSHIMURA et al.

Serial No. 09/375,007

Group Art Unit: 2871

Confirmation No. 4454

Filed: August 16, 1999

Examiner: Tai V. Duong

For: A FLAT DISPLAY UNIT AND METHOD FABRICATING SAME

**LETTER TO THE EXAMINER SUBMITTING  
CORRECTED AND/OR FORMAL DRAWINGS**

**Box ISSUE FEE**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

ATTENTION: OFFICIAL DRAFTSPERSON

Sir:

In accordance with the Notice of Allowability mailed September 15, 2004, Applicants herewith submits **10** sheets of corrected formal drawings of **FIGS. 1A, 1B, 2A, 2B, 2C, 2D, 3, 4, 5A, 5B, 5C, 5D, 5E, 6A, 6B, 6C, 6D, 7A, 7B, 8A, 8B, 8C, 8D, 9A, 9B, 10A, and 10B** for filing in the subject application.

It is respectfully requested that the corrected formal drawings filed herewith be entered in the above-referenced application.

Respectfully submitted,

STAAS & HALSEY LLP

Date: December 15, 2004

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